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### Information recording system.

In an information recording system, a semiconductor laser is energized in response to a drive current which is modulated in accordance with a recording signal in a recording mode so that a modulated laser beam is generated from the semiconductor laser. The modulated laser beam is focused on an optical disk and is reflected from the optical disk and the reflected laser beam is detected by a photo-detector. A detection signal from the detector is compared with a predetermined threshold level which is set in accordance with the peak intensity by a binary circuit and a relationship between the recording signal and an output signal from the binary circuit is detected by a detection circuit. In an overwrite mode in which the laser beam is focused on pit or pits on the optical disk, an overwrite detection signal is generated from the detection circuit and the overwrite detection signal is counted by a counter circuit. When the counter circuit counts a predetermined count value, the counter circuit generates a recording inhibition signal for preventing the semiconductor laser from generating the modulated laser beam.

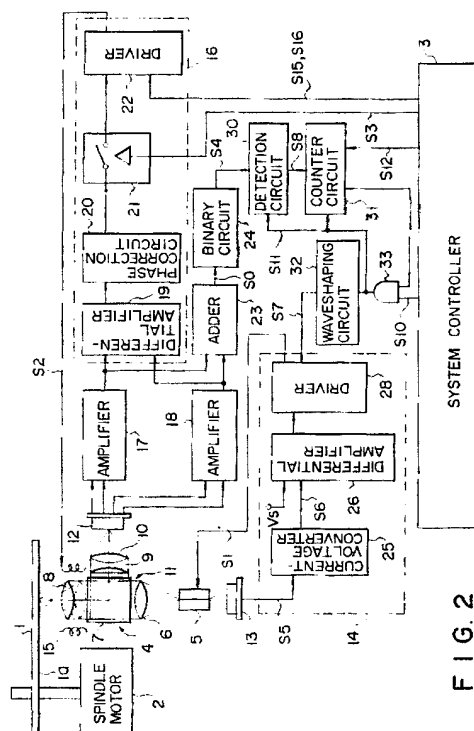


FIG. 2

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## INFORMATION RECORDING SYSTEM

The present invention relates to an information recording system for recording information on an optical information recording medium and, more particularly, to an information recording system which can prevent overwrite in an area of an information recording medium on which information has been already recorded.

In a conventional information recording system such as an optical disk system for optically recording or retrieving information in or from an optical information recording medium such as a write once or erasable optical disk, a retrieving light beam having a relatively low predetermined level is continuously generated from a semiconductor laser serving as a light source when the system is in a retrieval mode, and the information on the optical disk is retrieved by the retrieving light beam. When the system is a recording mode, a pulsating recording light beam having a relatively high predetermined reference level is generated. The level is intermittently changed in accordance with information to be recorded. Information is recorded on the optical disk by the recording light beam. In such an information recording system, when a light beam having a level higher than the predetermined reference level is radiated on a recording area of the optical disk, indeterminate information may often be written in the optical disk and destroy the information which was previously written, even if there was no intention to record in that area.

A technique for preventing such destruction of information already written in an optical disk is disclosed in, e.g., Japanese Patent Disclosure (Kokai) No. 61-258335. In this system, as will be explained below and as is understood from the waveforms shown in Figs. 1A, 1B, and 1C, when a pit on the recording medium, i.e., information, is detected by the system when it is in the recording mode, it is determined that overwriting may occur, and the recording operation is interrupted.

Fig. 1A shows a waveform of a light beam reflected from an optical disk which is detected when the system is in a retrieval mode. As is apparent from Fig. 1A, a light beam having a relatively low level is reflected from the optical disk, and low-level components are generated in the retrieving light beam as dark components corresponding to pits on the optical disk. These components are converted into significant data by a processing circuit including a detector.

Fig. 1B shows a waveform of a light beam reflected from the optical disk obtained when information, i.e., data, is being normally recorded in the optical disk, i.e., when data is recorded in a

non-recorded area. As is apparent from Fig. 1B, a relatively high-level pulsative waveform corresponding to the recorded data is detected.

Fig. 1C shows a waveform of a light beam reflected from the optical disk when data is overwritten in the optical disk, i.e., when data is being recorded in an area in which data has been already recorded. As shown in Fig. 1C, a light beam with a waveform obtained by superposing the waveform of a reflected light beam in the normal recording mode shown in Fig. 1B onto the waveform of the reflected light beam in the retrieval mode shown in Fig. 1A is generated upon overwriting. In a conventional system, when the waveform of the reflected light beam shown in Fig. 1C is compared with a predetermined threshold level  $T_h$  which is sufficiently lower than the intensity of the retrieving light beam, and a pit, i.e., a dark component, is detected, it is determined that overwriting has occurred. In other words, it is determined whether an area in which data is being recorded is a non-recorded area or a recorded area in accordance with whether a signal in the recording mode includes a signal corresponding to a pit, thus preventing overwrite.

A signal in the recording mode, in particular, a low-level signal component, is, however, adversely affected by, e.g., undershooting of a signal representing a recording beam intensity. The undershooting may cause the signal as shown in Fig. 1D, to fall below the threshold level  $T_h$  erroneously, resulting in improper a stopping of the recording operation. In addition, a detection error or omission may occur, and overwrite cannot be reliably prevented.

U.S. Application Serial No. 516,968 (Ryoji Takeuchi, Tomohisa Yoshimaru, and Akihiko Doi) filed April 27, 1990 describes a technique wherein overwrite is prevented by means of comparing the light beam reflected from an optical disk with the threshold level  $T_h$  in the recording mode. However, this technique has a drawback in the case where the optical disk currently in use is replaced by one having a higher or lower reflectance. The intensity of the light beam reflected from an optical disk having a higher reflectance will naturally be greater than that of the light beam reflected from an optical disk of lower reflectance, and the level of the signal corresponding to the light beam reflected from the former will also be higher than that of the latter. In the event that overwrite occurs, the level of the signal corresponding to the light beam reflected from a higher-reflectance optical disk will decrease; but may however, remain at a level higher than the predetermined threshold level  $T_h$ , with the result

that the overwrite cannot be detected. Conversely, the intensity of the light beam reflected from an optical disk having a lower reflectance will be lower than that of the light beam reflected from an optical disk of higher reflectance, and the level of the signal corresponding to the light beam reflected from the former will also be lower than that of the latter; therefore, a mere decrease in the signal level may be detected as overwrite.

It is an object of the present invention to provide an information recording system for recording information in an information recording medium, and preventing a detection error or a failure to reliably prevent overwrite.

It is another object of the present invention to provide an information recording system for successfully detecting overwrite irrespective of slight variations in the light beam reflected from the information recording medium.

According to this invention, a system for optically recording information in an optical recording medium comprises:

optical means for generating a light beam and guiding the light beam to said optical recording medium;

means for detecting a reflected light beam having a peak intensity level from said optical recording medium and converting said light beam into an electrical signal;

comparing means for comparing the level of said electrical signal from said converting means with a predetermined level to generate a comparison signal if said level of said signal exceeds said predetermined level, said comparing means includes means for setting the predetermined level in accordance with the peak intensity level of said light beam; and

means for inhibiting the optical recording operation of said system in accordance of the comparison signal.

According to the invention, there is also provided a system for optically recording information in an optical recording medium, comprising:

recording signal generating means for generating a signal to be recorded;

optical means for generating a recording light beam modulated in response to the recording signal, and guiding the recording light beam to said optical recording medium;

means for detecting a reflected light beam having peak intensity levels from said optical recording medium and converting said reflected light beam into an electrical signal;

comparing means for comparing the level of said electrical signal from said converting means with predetermined level to generate a comparison signal if said level of said electrical signal exceeds said predetermined level, said comparing means

includes means for setting the predetermined level in accordance with the peak intensity levels of said light beam;

detecting means for detecting the absence of said comparison signal and generating a recording inhibition signal in response to said absence; and

inhibiting means for inhibiting supply of the recording signal to said optical means in response to the recording inhibition signal.

According to the present invention, overwrite is detected utilizing the fact that the intensity of a recording light beam output from an optical means and reflected from a recorded area of an optical information recording medium is different from the intensity of a similar beam reflected from a non-recorded area. When a recording light beam modulated by the optical means is directed to the recording medium, the level of the light beam reflected from the recording medium is compared with a predetermined level. When a reflected light beam having a level below the predetermined level is detected by the detecting means, it is determined that a given area is a recorded area, and a recording inhibition signal is output to stop the recording operation.

In addition, a signal level below the predetermined level is counted by a counting means. When the count value exceeds a predetermined value, it is determined that a given area is a recorded area, and a recording inhibition signal is output to stop a recording operation.

According to the system of the present invention, since the intensity of the reflected light beam is detected, overwrite can be stably detected. In addition, because the system can determine overwrite after a plurality of reflected light beams having levels below the predetermined level are detected, overwrite can be prevented more reliably.

This invention can be more fully understood from the following detailed description when taken in conjunction with the accompanying drawings, in which:

Figs. 1A, 1B, and 1C respectively show waveforms of light beams reflected from a conventional optical disk in a retrieval mode, a normal recording mode, and an overwrite recording mode;

Fig. 1D shows an electric signal corresponding to the intensity level of the light beam shown in Fig. 1C;

Fig. 2 is a block diagram showing a schematic arrangement of an optical disk system according to an embodiment of the present invention;

Fig. 3A shows a waveform of a recording light beam reflected from the optical disk in the normal recording mode;

Fig. 3B shows a waveform of a recording light beam reflected from the optical disk in the over-

write recording mode;

Fig. 4 is a circuit arrangement showing a binary circuit shown in Fig. 2;

Figs. 5A and 5B show waveforms of sum signal outputted from an adder;

Fig. 6A is a waveform chart showing a recording signal output from a system controller shown in Fig. 2;

Fig. 6B is a waveform chart showing a signal from an adder and corresponding to a recording light beam reflected from the optical disk shown in Fig. 2;

Fig. 6C is a waveform chart showing a binary signal output from a binary circuit shown in Fig. 1;

Fig. 6D is a waveform chart showing a detection signal output from a detection circuit shown in Fig. 2;

Fig. 6E is a waveform chart showing a count signal generated in a counter circuit shown in Fig. 2;

Fig. 6F is a waveform chart showing write permission and inhibition signals output from the counter circuit shown in Fig. 2;

Fig. 7 is a block diagram showing a detection circuit and a counter circuit shown in Fig. 2;

Fig. 8 is a block diagram showing a schematic arrangement of an optical disk system according to another embodiment of the present invention; and

Fig. 9 is a block diagram showing a detection circuit shown in Fig. 8.

Fig. 2 shows a schematic arrangement of an optical disk system serving as an information recording system according to the present invention. Referring to Fig. 2, an optical information recording medium, i.e., an optical disk 1, is formed by coating a metal coating layer made of, e.g., tellurium or bismuth on the surface of a circular substrate of, e.g., a glass or plastic to have a doughnut-like shape. The optical disk 1 is rotated by a spindle motor 2. The start, stop and speed of rotation of the spindle motor 2 are controlled by a motor controller (not shown) operated in response to a control signal from a system controller 3. The system controller 3 includes, e.g., a microcomputer, and controls the rotation of the spindle motor 2.

An optical head 4 is disposed below the optical disk 1. A light beam is focused on the optical disk 1 by the optical head 4, and data is optically recorded in or retrieved from the optical disk 1. The optical head 4 includes a semiconductor laser 5, a collimator lens 6, a polarization beam splitter 7, an objective lens 8, a known astigmatism optical system 11 having a cylindrical lens 9 and a convex lens 10, and photodetectors 12 and 13 each having four-divided detection areas. The optical head 4 is disposed to be movable in the radial direction of

the optical disk 1 by a linear actuator (not shown) consisting of, e.g., a linear motor, and is moved by the linear actuator to trace a target track which is a target for recording or retrieval by a light beam in response to an instruction from the controller 3.

The semiconductor laser 5 is energized in response to a drive signal S1 output from a light beam control circuit 14 to generate a divergent laser beam. When information is recorded on a recording film 1a of the optical disk 1, the drive signal S1 is modulated in correspondence with the information to be recorded. The semiconductor laser 5, therefore, generates an intense laser light beam having a modulated light intensity in accordance with the information to be recorded. When the information is read out from the recording film 1a of the optical disk 1, and is retrieved, a weak laser beam having a constant light intensity is generated from the semiconductor laser 5.

The divergent laser light beam generated from the semiconductor laser 5 is converted into a parallel light beam by the collimator lens 6, and is guided to the polarization beam splitter 7. The laser beam guided to the polarization beam splitter 7 is transmitted through the polarization beam splitter 7, and is incident on the objective lens 8. The incident beam is focused on the recording film 1a of the optical disk 1 by the objective lens 8.

The objective lens 8 is supported by a lens actuator 15 serving as a lens drive mechanism to be movable along a direction of the optical axis. The objective lens 8 is moved along the direction of the optical axis in response to a servo signal S2 from a focus servo circuit 16 to be kept in an in-focus state. When the objective lens 8 is kept in the in-focus state, a laser light beam from the objective lens 8 is focused on the surface of the recording film 1a of the optical disk 1, and a minimum beam spot is formed on the surface of the recording film 1a of the optical disk 1.

The objective lens 8 is supported by the lens actuator 15 to be also movable along a direction perpendicular to the optical axis. The objective lens 8 is moved along the direction perpendicular to the optical axis in response to a servo signal from a tracking servo circuit (not shown), and is kept in the in-track state. In this in-track state, a light beam focused on the surface of the recording film 1a of the optical disk 1 from the objective lens 8 traces a recording track formed on the surface of the recording film 1a of the optical disk 1. The objective lens 8 is kept at an in-focus state and in the in-track state, and information can be written in or read out from the optical disk 1.

A divergent laser beam reflected from the recording film 1a of the optical disk 1 is converted into a parallel beam by the objective lens 8 in the in-focus state, and is returned to the polarization

beam splitter 7. The beam is reflected by the polarization beam splitter 7, and is guided to the photodetector 12 having the four-divided detection areas by the astigmatism optical system 11 including the cylindrical and convex lenses 9 and 10. The shape of a light beam spot formed on the detector 12 by a light beam is changed in accordance with a defocus state. Two pairs of signals output from two photodetection areas diagonally located in the photodetector 12 are supplied to amplifiers 17 and 18, respectively.

The focus servo circuit 16 includes a differential amplifier 19 for receiving and differentially amplifying the two signals respectively amplified by the amplifiers 17 and 18, a phase correction circuit 20 for correcting the phase of an output signal from the differential amplifier 19, an analog switch 21 for controlling whether an output signal from the phase correction circuit 20 is supplied to a driver 22, and the driver 22 for amplifying a signal from the analog switch 21 to drive the actuator 15. When the analog switch 21 is turned on in response to a focus-on signal S3 from the system controller 3, a signal from the phase correction circuit 20 is supplied to the actuator 15 through the driver 22, thus operating a focus servo loop.

In addition, the output signals from the amplifiers 17 and 18 are also supplied to an adder 23, and are added to each other. The adder 23 outputs a sum signal so having a signal level corresponding to the data retrieved from or recorded in the optical disk 1. More specifically, in a retrieval mode, a sum signal including a low-level component shown in the waveform in Fig. 1A is output from the adder 23. In a recording mode, the optical disk 1 is irradiated with the recording light beam having a low intensity substantially same as that of the retrieving light beam and a high intensity which is ten to thirty times of that of the retrieving light beam, in general, twenty times of that of the retrieving light beam, i.e., the recording light beam having the low level of 0.5 mW and the high level of 10 mW. Thus, the pit or pits are formed on the optical disk 1 to record data. In a normal recording mode, i.e., when data is recorded in a non-recorded area on the optical disk, a sum signal having a same waveform as that of the recording light beam which is reflected from the optical disk 1 is output from the adder 23, as shown in Fig. 3A. In contrast, in an overwrite recording mode, i.e., when data is further recorded in an area which data has already been recorded, a sum signal which is different with the recording light beam is output from the adder 23, as shown in Fig. 3B. The sum signal from the adder 23 has a waveform which is not coincided with that of the recording light beam and includes an intermediate level component which is not appeared in the recording light beam. More specifi-

cally, when the recording light beam is focused on a recorded area in which the pit or pits have been already formed, the light beam is diffracted by the pit. As a result, the light intensity of the light beam reflected from the pit is reduced, and the light beam is converted into an intermediate level component smaller than the high-level component, and larger than the low-level component. The intermediate level component have a level which is within a range of 1/4 to 2/3 times of the high-level component. in general, 1/3 times of the high-level component.

The output signal from the adder 23 is transmitted to a binary circuit 24. As can be seen from Fig. 4, a binary circuit 24 is constituted by a circuit including a comparator 24a, resistors R1, R2, R3, R4, and R5, a diode D1, and a capacitor C1. The output of an adder 23 is supplied through the resistor R1 to the non-inverted signal input terminal (+) of the comparator 24a. Applied to the inverted signal input terminal (-) of the comparator 24a is a voltage produced as a result of voltage division performed by the resistors R3 and R4. The level of the voltage applied to this inverted signal input terminal is a detection level, i.e. the threshold voltage Th, which is determined as follows:

When the power source voltage is VDD, a potential VA1 at point A and a potential VB1 at point B, under the condition wherein no output is supplied from the adder 23, are represented by:

$$VA1 = VDD \times \{(R3 + R4)/(R2 + R3 + R4)\} \quad (1)$$

$$VB1 = VDD \times \{R4/(R2 + R3 + R4)\} \quad (2)$$

When a voltage signal corresponding to the recording pulse reflected light beam is input from the adder 23, the capacitor C1 is charged with electricity supplied through both the resistor R1 and the diode D1, in which case a charging time constant  $\tau1$  and a discharging time constant  $\tau2$  are determined by:

$$\tau1 = R1 \times C1 \quad (3)$$

$$\tau2 = (R3 + R4) \times C1 \quad (4)$$

Further, when the resistors R1, R2, and R4 are determined so as to satisfy the relation  $\tau1 < \tau2$ , the potential at point A is substantially the same as a peak value VP of the output of the adder 23; that is:

$$VA2 \cong VP \quad (5)$$

In short, a voltage the envelope of which passes through the peaks of the signal corresponding to the reflected beam can be produced as shown in Figs. 5A and 5B.

A potential VB2 at point B is represented by:

$$VB2 \cong VP \times \{R4/(R3 + R4)\} \quad (6)$$

Thus, a potential which is lower by a predetermined value than the peak value VP of the signal corresponding to the reflected beam can be produced as shown in Fig. 5A. Even if the peak value VP varies due to differences in the reflectances of

individual optical disks or variations in the output of the semiconductor laser 5, the potential at point B, i.e. the threshold level  $T_h$ , varies after a predetermined period of time, in accordance with a variation in the peak value  $V_P$ . Thus, the threshold level  $T_h$  is always set at an optimum value. The threshold level  $T_h$  is set to have a level which is within a range of 1.5 to 2.5 times of the intermediate level, i.e., a range of 2.5/3 to 1.5/3 times of the high level, preferably, a range of 0.8 to 0.5 times of the high level.

If resistors having such resistances as allow the potential  $VB_1$  to be lower than the average value of the potential  $VB_2$  are selected as the resistors  $R_2$ ,  $R_3$ , and  $R_4$ , fluctuation in the potential at point B can be minimized as shown in Fig. 5B, even in the case where the peak value  $V_P$  varies considerably in the early period of the writing operation or the light beam passes through defective portions in an optical disk -- such as pinholes -- thus improving the reliability of the detecting operation.

If the polarity of the diode  $D_1$ , the polarity of the comparator 24a, and the potential of the resistor  $R_2$  which appears on the side of the resistor  $R_2$  opposite to point A are changed in accordance with the output polarity of the adder 23 and the intensity of the reflected light beam in the recording mode, fluctuation in the potential at point B can be minimized irrespective of the polarities of the potentials shown in Figs. 5A and 5B.

In this manner, the binary circuit 24 performs digitization by comparing the analog signal output from the adder 23 with the threshold level  $T_h$ , the threshold level  $T_h$  being automatically set at an optimum value which is lower by a predetermined amount than the peak value of the signal corresponding to the reflected light beam, as has been mentioned previously. A reflected light beam signal  $S_4$ , produced as a result of the digitization performed by the binary circuit 24, is supplied to a detection circuit 30.

The detection circuit 30 receives recording data  $S_{11}$  shown in Fig. 6A output from the system controller 3 through an AND gate 33, and the binary signal  $S_4$  shown in Fig. 6C output from the binary circuit 24. In an overwrite situation mode, an overwrite detection signal  $S_8$  is generated. More specifically, if the optical disk 1 in which data is to be recorded is a non-recorded disk, the binary signal is output from the binary circuit 24 in correspondence with the recording data  $S_{11}$  or  $S_{10}$ . The detection circuit 30, therefore, checks whether the recording data  $S_{11}$  has a one-to-one correspondence with the binary signal  $S_4$  from the binary circuit 24. When a binary signal  $S_4$  corresponding to the recording data  $S_{11}$  is not generated, the overwrite detection signal  $S_8$  is generated from the detection circuit 30.

As shown in Fig. 7, the detection circuit 30 includes a flip-flop 30A, a NOR gate 30C, and an inverter 30B. As will be described later, the overwrite detection signal  $S_8$  is output from the Q output terminal of the flip-flop 30A in accordance with the binary signal  $S_4$  from the binary circuit 24 and the recording data  $S_{10}$  from the system controller 3. The overwrite detection signal  $S_8$  from the detection circuit 30 is supplied to a counter circuit 31.

The counter circuit 31 counts the number of detected overwrites in response to the overwrite detection signal  $S_8$  output from the detection circuit 30. When the count reaches a predetermined number, i.e., 40, a recording inhibition signal  $S_9$  is output to inhibit the recording operation. As shown in Fig. 7, the counter circuit 31 includes counters 31A and 31B, a NAND gate 31C, and an inverter 31D, as will be described later. The pulses of the recording data  $S_{10}$  are counted while the overwrite detection signal is kept at a high level. When the count reaches a predetermined number, a low-level recording inhibition signal  $S_9$  is output from the NAND gate 31C in response to the high-level signals output from the counters 31A and 31B. The counter circuit 31 is arranged such that the recording inhibition signal  $S_9$  is output from the counter circuit 31 only when it is detected that the count has reached the predetermined count. This is because if the optical disk 1 has a defect in which the reflective film is partially removed from the beginning, e.g., a pin hole is formed, the overwrite detection signal  $S_8$  may be output even if data is recorded in a non-recorded area, thus causing a erroneous indication of overwrite. The predetermined count value is determined as follows. The upper limit of the predetermined count value is restricted in accordance with the error correction capability of the system. This is because the recording operation must be stopped while the data destroyed by overwriting can be corrected by an error correction circuit (not shown). On the other hand, the lower limit of the predetermined count value is determined in accordance with an allowance range of defects such as pin holes on the optical disk 1. More specifically, when data is recorded on a defective portion of the optical disk 1, output of the overwrite detection signal  $S_8$  is continued. If the recording inhibition signal  $S_9$  is output prior to passing through the defective portion, however, the defective portion is erroneously detected, and it is determined that an overwrite has occurred. Therefore, the predetermined count value must be determined to be a value larger than the minimum allowance count value. The recording inhibition signal  $S_9$  from the counter circuit 31 is supplied to an AND gate 33. Note that when a clear signal  $S_{12}$  is output from the system controller 3, the counter

circuit 31 is reset.

If overwrite occurs, with the result that a low level pulse signal as shown in Fig. 5B is input from the adder 23 to the binary circuit 24 for a relatively long period of time, the threshold level  $T_h$  will undesirably decrease such that it approaches the level of the above pulse signal, allowing a high level binary signal to be produced. To prevent the threshold level  $T_h$  from decreasing, a time constant  $\tau_1$  required for discharge from the binary circuit should be set longer than the period beginning at the time a counter circuit 31 starts counting up to a predetermined number, and ending at the time a recording inhibition signal S9 is generated. If the time constant  $\tau_1$  is set in this way, variation in the threshold level  $T_h$  can be prevented at the time of overwrite detection. Thus, in the case where the optical disk currently in use is replaced by an optical disk having a higher or lower reflectance, or the level of the reflected light beam decreases below its initial level and remains as such for a relatively long period of time, the recording inhibition signal S9 is generated by the counter circuit 31.

The AND gate 33 receives the recording data S10 supplied from an external unit (not shown) through the system controller 3 as one input, and receives the recording inhibition signal S9 output from the counter circuit 31 as the other input. The recording data is, therefore, output from the AND gate 33 while the counter circuit 31 generates a low-level permission signal, and the output of the recording data S11 from the AND gate 33 is inhibited while the counter circuit 31 generates a high-level inhibition signal. The recording data S11 is supplied to a waveshaping circuit 32, the detection circuit 30, and the counter circuit 31. The waveshaping circuit 32 shapes the recording data S11, and the shaped recording signal is supplied to a driver 28 (to be described later).

In the semiconductor laser 5, the photodetector 13 consisting of a photoelectric conversion element such as a photodiode is arranged to face a light emission point, at which a monitor light beam is emitted, which is opposite to a light emission point at which a recording or retrieval laser beam is emitted. A monitor light beam from the semiconductor laser 5 is guided to the photodetector 13. The monitor light beam is converted into an electrical signal by the photodetector 13. This electrical signal is supplied to the light beam count circuit 14 as a light beam monitor signal S5 from the semiconductor laser 5.

The light beam control circuit 14 receives the light beam monitor signal S5 output from the semiconductor laser 5. Feedback control is performed to maintain the light beam from the semiconductor laser 5 at a constant intensity. More specifically, a

current-voltage converter 25 receives the monitor signal S5 photoelectrically converted by the photodetector 13 and extracted as a current signal. This monitor signal is converted into a voltage signal S6 corresponding to the light intensity received by the photodetector 13, i.e., the light beam from the semiconductor laser 5, and the converted signal is output. The voltage signal S6 output from the current-voltage converter 25 is supplied to a differential amplifier 26.

The differential amplifier 26 receives the voltage signal S6 as one input, and receives a reference voltage  $V_s$  output from a constant voltage power source (not shown) as the other input. These voltages S6 and  $V_s$  are compared with each other, a difference therebetween is amplified, and the amplified difference is output as an error signal. The reference voltage  $V_s$  is a constant voltage to obtain a light beam required for retrieval. When feedback control is performed such that the voltage signal S6 approaches the reference voltage  $V_s$ , a constant light beam is output from the semiconductor laser 5. The error signal from the differential amplifier 26 is supplied to the driver 28.

The driver 28 receives a recording pulse signal S7 in accordance with information to be recorded from the above-mentioned waveshaping circuit 32. Therefore, a light beam modulated for a recording operation is output from the semiconductor laser 5. Note that, in the retrieval mode, the driver 28 receives a voltage signal output from the differential amplifier 26. In the recording mode, the driver 28 receives a voltage signal input in the retrieval mode which is set immediately before a recording operation, and held by a sample hold circuit (not shown). These two inputs are switched depending on whether the recording or retrieval mode is set. Regardless of the mode, feedback control is performed at the level of the light beam in the retrieval mode.

Operation of the system to present overwrite when the system is in the recording mode will be described hereinafter with reference to a timing chart shown in Figs. 6A to 6F.

Prior to a recording operation, an initial operation is performed to check the level of the light beam emitted from the semiconductor laser 5. More specifically, when the focus-off signal S3 is output from the system controller 3, the analog switch 21 is turned off. Therefore, the focus servo loop is disconnected, and the objective lens 8 is released from focusing control. Then, a backward movement signal S15 is supplied from the system controller 3 to the lens actuator 15 through the driver 22 to forcibly move the actuator 15. The objective lens 8 is, therefore, forcibly moved to a position represented by a dotted line in Fig. 2, and is kept in a defocus state. In this defocus state,

power is supplied to the light beam control circuit 14, the semiconductor laser 5 is energized, and the retrieval and monitor laser beams are output from the semiconductor laser 5. The monitor light beam generated from the semiconductor laser 5 is, therefore, converted into a current corresponding to the light beam by the photodetector 13, and is output as the light beam monitor signal S5. The current-voltage converter 25 converts the light beam monitor signal S5 into the voltage signal S6, and the voltage signal S6 is supplied to the differential amplifier 26. In the differential amplifier 26, the reference voltage  $V_s$  which is set in advance is compared with the voltage signal S6, and a difference therebetween is output as an error signal. The driver 28 is operated in response to this error signal. A drive signal is supplied from the driver 28 to the semiconductor laser to decrease the light beam from the semiconductor laser 5 if "voltage signal  $S6 > \text{reference signal } V_s$ ", and to increase the light beam from the semiconductor laser 5 if "voltage signal  $S6 < \text{reference signal } V_s$ ". When this error signal is supplied to the driver 28, a feedback loop is formed to perform control such that the reference voltage  $V_s$  is equal to the voltage signal S6. Therefore, the light beam from the semiconductor laser 5 can be kept at a constant level.

When a forward movement signal S16 is output from the system controller 3, the actuator 15 is driven by the driver 22, and the objective lens 8 is moved toward the position of an in-focus point. When the objective lens is moved near the in-focus point, the analog switch 21 is turned on, and the focus servo loop is connected, thus completing the initial operation. After the initial operation is completed, auto focus control is performed by the focus servo loop, and the objective lens is kept in the in-focus state. While the objective lens is kept in the in-focus state, data is read out from or written in the optical disk 1, i.e., a normal operation is started.

In such a state, as shown in Fig. 6A, an output of the pulsating recording data S10 from the system controller 3 is started. When this recording mode is started, the clear signal S12 is supplied from the system controller 3, so that the recording permission signal S9 output from the counter circuit 31 is initialized at high level. More specifically, a high-level clear signal is input from the system controller 3 to the input terminals CLR of the counters 31A and 31B shown in Fig. 7, and the counters 31A and 31B are cleared. At this time, the output S9 from the NAND gate 31C is kept at high level, and an output obtained by inverting the signal S9 by the inverter 31D is input to the NOR gate 30C. The recording signal S11 shown in Fig. 6B is, therefore, input to the input terminal CP of the flip-flop 30A, and the input terminals T and P of the

counters 31A and 31B through the NOR gate 30C. The flip-flop 30A is set at a leading edge of the recording signal shown in Fig. 6B, and is reset at a leading edge of the binary signal shown in Fig. 6C supplied through the inverter 30B. Therefore, the signal S8 having a waveform shown in Fig. 6D is output from the output terminal Q of the flip-flop 30A. The counters 31A and 31B detect the level of the signal S8 from the output terminal Q of the flip-flop 30A, and generate a count pulse when the signal S8 is set at high level. In the normal recording mode in which information is recorded in an area without a pit, therefore, the count pulse is not generated and the counters 31A and 31B are not counted up.

In the normal mode, this high-level signal is input to the AND gate 33. The recording data S10 is, therefore, supplied to the waveshaping circuit 32 through the AND gate 33, and is also supplied to the driver 28. Therefore, the semiconductor laser 5 intermittently emits a high-intensity laser light beam corresponding to the recording data S10. This laser light beam is converted into a parallel light beam by the collimator lens 6, and is guided to the polarization beam splitter 7. The laser beam guided to the polarization beam splitter 7 is transmitted through the polarization beam splitter 7, and is incident on the objective lens 8. The incident light beam is focused on the recording film 1a of the optical disk 1 by the objective lens 8. Therefore, a pit is formed on the recording film 1a, and information is recorded.

The divergent laser light beam reflected from the recording film 1a of the optical disk 1 is converted into a parallel light beam by the objective lens 8 kept in the in-focus state, and is returned to the polarization beam splitter 7. The returned beam is reflected by the polarization beam splitter 7, and an image is formed on the photodetector 12 by the astigmatism optical system 11 constituted by the cylindrical and convex lenses 9 and 10. At this time, if the area of the optical disk 1 in which information is written is a recorded area, a reflected light waveform having a decreased light intensity level is generated, as shown in Fig. 6B. The signals photoelectrically converted by the photodetector 12 are supplied to the amplifiers 17 and 18, respectively. One of the signals amplified by the amplifiers 17 and 18 is supplied to the focus servo circuit 16 including the differential amplifier 19, and focusing control is performed. The other of the signals amplified by the amplifiers 17 and 18 is supplied to the adder 23, and is added. Thereafter, the obtained signal is supplied to the binary circuit 24.

As shown in Figs. 3B and 6B, when the signal from the adder 23 is compared with the predetermined threshold level  $Th$ , the binary circuit 24 outputs the binary reflection light signal S4 shown



in Fig. 6C. At this time, a laser light beam radiated on the pit which has been already formed does not appear in the signal S4 because the reflection light beam is small, as shown in Fig. 6B. The reflection light signal S4 is supplied to the detection circuit 30.

As shown in Fig. 7, in the detection circuit 30, an output from the binary circuit 24 is inverted by the inverter 30B, and is input to the input terminal K of the flip-flop 30A. In addition, the recording signal is input to the flip-flop 30A through the NAND gate 30C. The flip-flop 30A is, therefore, set at the leading edge of the recording signal, and is reset at the leading edge of the binary signal S4, as shown in Fig. 6D. As shown in Fig. 6C, the output of the binary signal S4 is slightly delayed from an output of the recording signal supplied through the NOR gate 30C.

As has been described above, in the normal recording mode, a signal S8 having a high level is periodically output from the flip-flop 30A when the flip-flop 30A is alternately set and reset, as shown in Figs. 6D, and this output periodically is input to the counter 31A. The recording signal is input to the counters 31A and 31B through the NOR gate 30C and an inverter 30D. The counter 31A and 31B will operate to count the trailing edges of the recording signal from NOR gate 30C, only if the signal from flip-flop 30A is high. Therefore, if the binary signals are periodically generated, the flip-flop 30A is not maintained at high so that the counters 31A and 31B cannot count the recording signals.

In the overwrite recording mode, the binary signal is periodically not output. As shown in Fig. 6D, therefore, the output S8 from the flip-flop 30A is kept at high level, and the counters 31A and 31B start counting in accordance with the falling edge of the recording data S11, as shown in Fig. 6E. When the counters 31A and 31B count to the predetermined value, high-level signals are input from the counters 31A and 31B to the NAND gate 31C, and a low-level signal is output from the NAND gate 31C, as shown in Fig. 6F, thus disabling the AND gate 33. As a result, the recording data is not supplied to the driver 28, and the writing by the laser beam is inhibited. Such a recording inhibition state is maintained until the clear signal S12 is output from the system controller 3. Therefore, an overwrite operation can be prevented.

Thus, an overwrite operation is not immediately determined when a component having a small amount of reflected light from the optical disk 1 is detected. Overwrite is determined when the number of components having a small amount of reflected light reaches a predetermined number. Therefore, detection errors or omissions can be significantly suppressed.

In addition, the data contained in a recorded area is destroyed until the predetermined number of components having a small reflection light amount are detected from the optical disk 1. However, the amount of destroyed data is set to fall within a range which allows error correction, and the reliability of the recorded data is not damaged.

Although in the above description it is determined that overwrite occurs when a predetermined number of components having a small amount of reflected light are detected, overwrite may be determined even if the count does not reach the predetermined count. More specifically, as shown in Fig. 8, the counter circuit 31 is not provided, and the output S9 from the detection circuit 30 is directly input to the AND gate 33. More specifically, such a detection circuit 30 has an arrangement shown in Fig. 9. In the circuit shown in Fig. 9, the flip-flop 30A is set at a leading edge of the recording data S11 output from the AND gate 33 and input to the input terminal CP of the flip-flop 30A, and is reset in response to the binary signal S4 input to the input terminal R of the flip-flop 30A from the binary circuit 24 through the inverter 30B. As shown in Fig. 6D, the output S8 from the flip-flop 30A is input to the input terminal D of a second flip-flop 30E through an OR gate 30G as the overwrite detection signal S8. In addition, the input terminal CP of the flip-flop 30E receives the recording signal S11 shown in Fig. 6A through an inverter 30F. In the normal recording mode, since the leading edge of the recording signal shown in Fig. 6A is output in synchronism with the leading edge of the output signal from the flip-flop 30A shown in Fig. 6D, an output from the output terminal Q of the flip-flop 30E is kept at low level. Therefore, the output signal S9 from an inverter 30H obtained by inverting the output from the output terminal Q is kept at high level, and the AND gate 33 is opened. The recording data S10 is supplied to the driver 28a, and the laser beam is modulated.

In contrast to this, upon overwriting, since the leading edge of the recording signal shown in Fig. 6A is not in synchronism with the leading edge of the output signal from the flip-flop 30A shown in Fig. 6D, an output from the output terminal Q of the flip-flop 30A goes to high level. Since this output is returned to the input terminal D of the flip-flop 30E through the OR gate 30G, the output from the output terminal Q of the flip-flop 30E is kept at high level. Therefore, the output signal S9 from the inverter 30H obtained by inverting the output from the output terminal Q is kept at low level. The AND gate 33 is disabled, and the recording data S10 is not supplied to the driver 28. After a write inhibition signal is output and a predetermined time period elapses, a reset signal is input from the system

controller 3 to the input terminal R of the flip-flop 30E. The flip flop 30E is reset, and the circuit is returned to its initial state.

Thus, it is determined that overwrite occurs immediately after a component having a small amount of reflected light from the optical disk 1 is detected, and the write operation is inhibited.

As described above, utilizing the fact that the intensity of the recording light beam reflected from a recorded area of the optical disk 1 is different from the intensity of the light beam reflected from a non-recorded area, the system will detect when an overwrite situation has developed. When the recording light beam reflected by the optical disk 1 is compared with the predetermined threshold level, the light beam is digitized. A pulse having a level below the predetermined level is detected from the binary signal by the detection circuit 30. When the level reaches the predetermined level, it is determined that the area in which information is written is a recorded area, and a write inhibition signal is output to stop the recording operation. In addition, the number of signals below the predetermined level is counted by the counter circuit 31. When the count exceeds a predetermined value, it is determined that the area in which information is written is a recorded area, and a write inhibition signal is output to stop the recording operation. In addition, since it is determined that an overwrite has occurred only after a plurality of reflection light components having a level below the predetermined level are detected, overwrite can be reliably prevented.

A case wherein, e.g., the detection circuit 30 and the counter circuit 31 are operated using the leading and trailing edges of the recording data S10 (S11) has been described in the above embodiment. However, the phases of the recording data S10 (S11) and the reflected light signal S4 change depending on, e.g., the recording method or an arrangement of the waveshaping circuit 32. If an operation timing is properly changed depending on the recording method or the arrangement of the wave-shaping circuit 32, therefore, the same effect as in the above embodiment can be obtained.

As has been described above in detail, according to the present invention, there is provided an information recording system which can prevent a detection error or omission and thus reliably prevent overwriting.

As described above, even in the case where the optical disk currently in use is replaced by an optical disk having a higher/lower reflectance, such that the intensity of the light beam reflected from the latter is slightly higher/lower than that of the former, the threshold level Th is adjusted in accordance with the intensity of the light beam as reflected from the optical disk of higher/lower reflectance.

Thus, according to the present invention, overwrite can always be detected.

## Claims

1. A system for optically recording information in an optical recording medium (1), comprising: optical means (5, 8) for generating a light beam and guiding the light beam to said optical recording medium (1); and means (12) for detecting a reflected light beam having a peak intensity level from said optical recording medium (1) and converting said light beam into an electrical signal; characterized by further comprising: setting means (24) for setting a predetermined level in accordance with the peak intensity level of said light beam; comparing means (24) for comparing the level of said electrical signal from said converting means with the predetermined level to generate a comparison signal if said level of said signal exceeds said predetermined level; and means (30, 31, 33) for inhibiting the optical recording operation of said system in accordance with comparison signal.
2. A system according to claim 1, characterized in that said setting means (24) sets the predetermined level in accordance with the average value of the peak intensity level of the reflected light beam within a predetermined time interval.
3. A system according to claim 1, characterized in that said optical means (5, 8) generates a pulsative light beam, said detecting means (12) generates a pulsative electric signal in correspondence with said pulsative light beam, and said setting means (24) sets the predetermined level in accordance with an envelope passing through peaks of said pulsative electric signal.
4. A system according to claim 1, characterized in that the predetermined level set by said setting means (24) is equal to or higher than a certain minimum value.
5. A system according to claim 1, characterized in that said setting means (24) includes a charge/discharge circuit (R2, R3, R4, C1, D1) to which is applied a constant voltage (VDD) and said electrical signal supplied from said converting means (12), and which is discharged in accordance with a variation in said electrical signal supplied from said converting means (12).
6. A system according to claim 5, characterized in that said charge/discharge circuit (24) has a charging time constant which determines the charging time and a discharging time constant which determines the discharging time, said discharging time constant being greater than said charging time

constant.

7. A system for optically recording information in an optical recording medium (1), comprising:

recording signal generating means (3) for generating a signal to be recorded;

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optical means (5, 8) for generating a recording light beam modulated in response to the recording signal, and guiding the recording light beam to said optical recording medium (1);

means (12) for detecting a reflected light beam having peak intensity levels from said optical recording medium (1) and converting said reflected light beam into an electrical signal; characterized by further comprising:

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setting means (24) for setting a predetermined level in accordance with the peak intensity levels of said light beam;

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comparing means for comparing the level of said electrical signal from said converting means with the predetermined level to generate a comparison signal if said level of said electrical signal exceeds said predetermined level;

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detecting means (30, 31) for detecting the absence of said comparison signal and generating a recording inhibition signal in response to said absence; and

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inhibiting means (33) for inhibiting supply of the recording signal to said optical means (5, 8) in response to the recording inhibition signal.

8. A system according to claim 7, characterized in that said detecting means (30, 31) includes counting means (31) for counting the number of times the comparison signal is absent, and generating the recording inhibition signal when the count reaches a predetermined count.

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9. A system according to claim 7, characterized by further comprising:

means (3) for generating a release signal for cancelling the recording inhibition signal and supplying said release signal to said detecting means (30, 31).

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10. A system according to claim 7, characterized in that said detecting means (30, 31) includes means (30) for comparing the timing of said recording signal and said comparison signals.

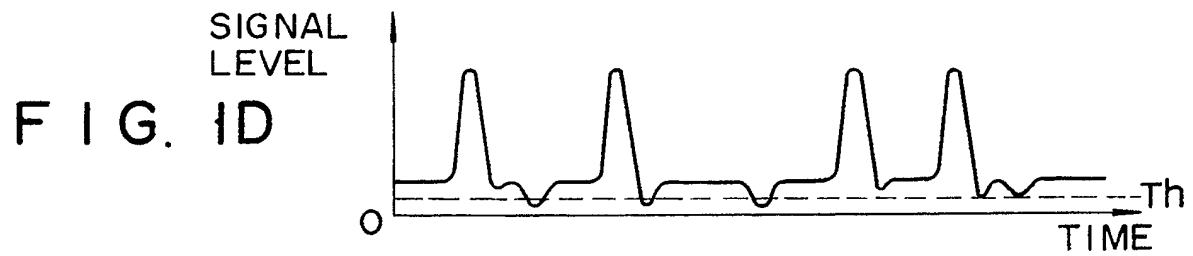
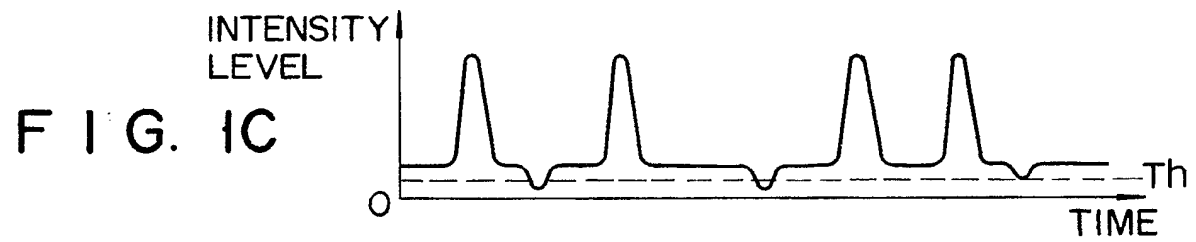
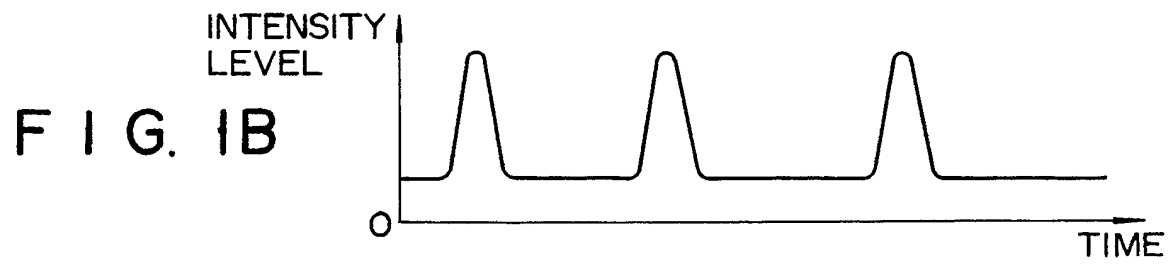
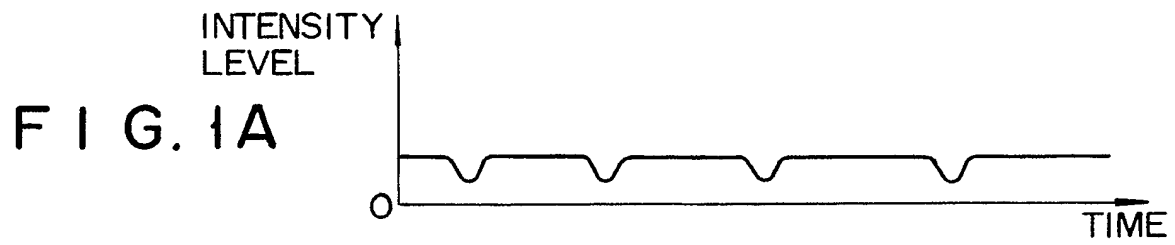
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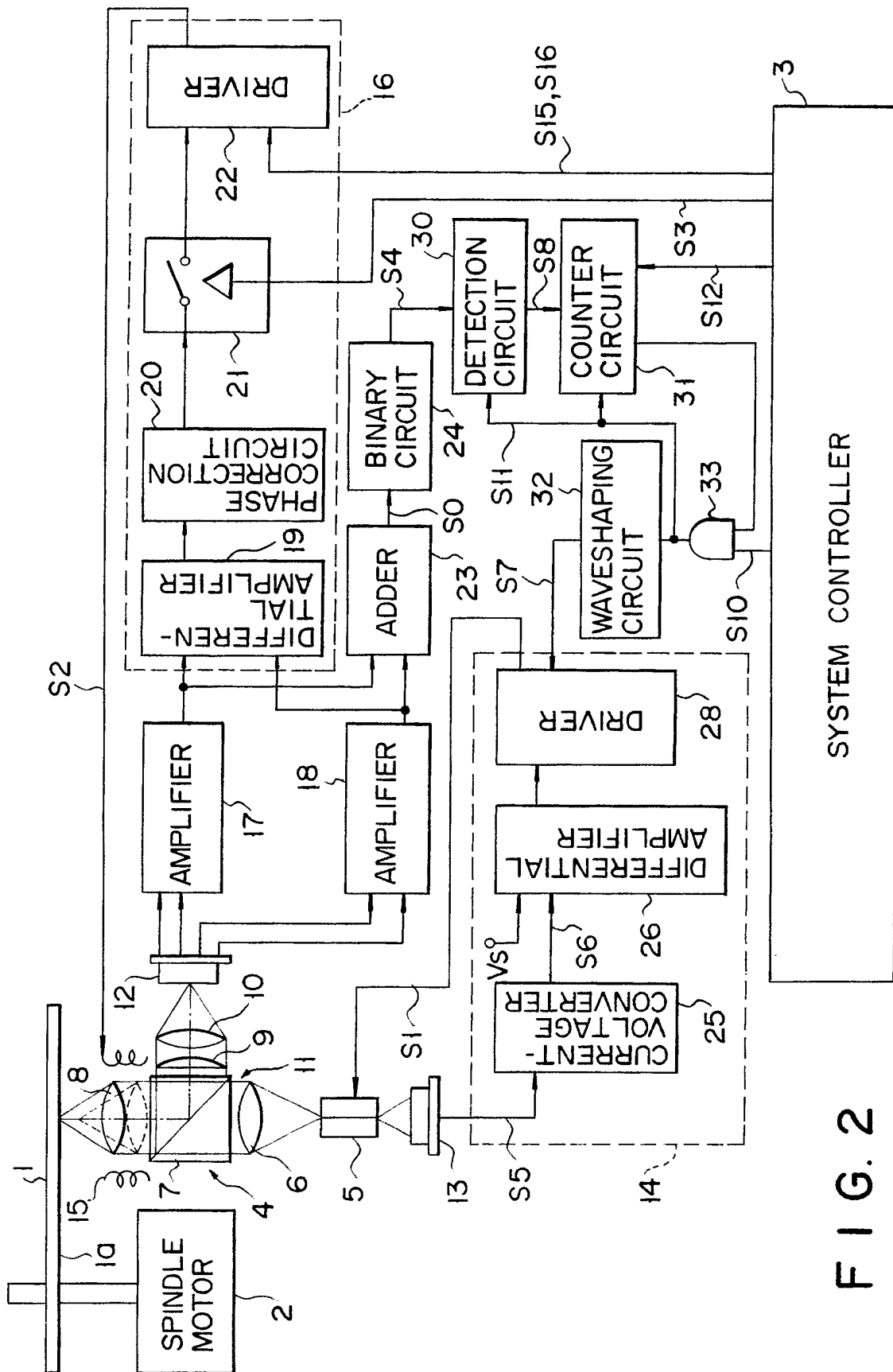
11. A system according to claim 7, characterized in that said detecting means (30, 31) includes means (30) for continuously outputting the recording inhibition signal when the recording inhibition signal is to be output.

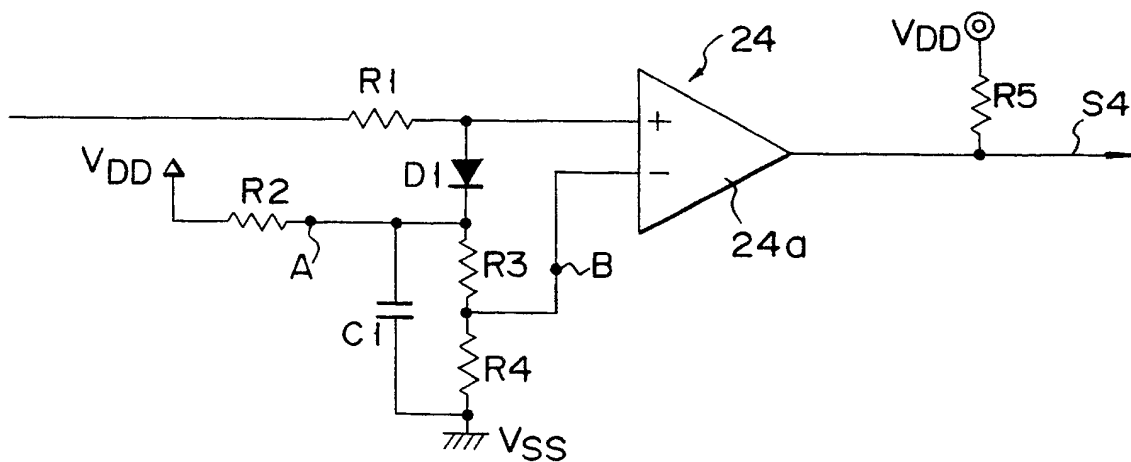
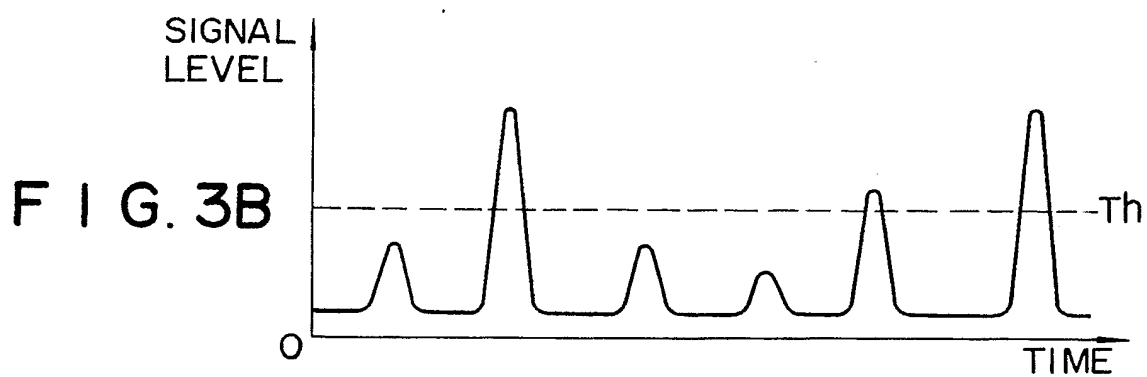
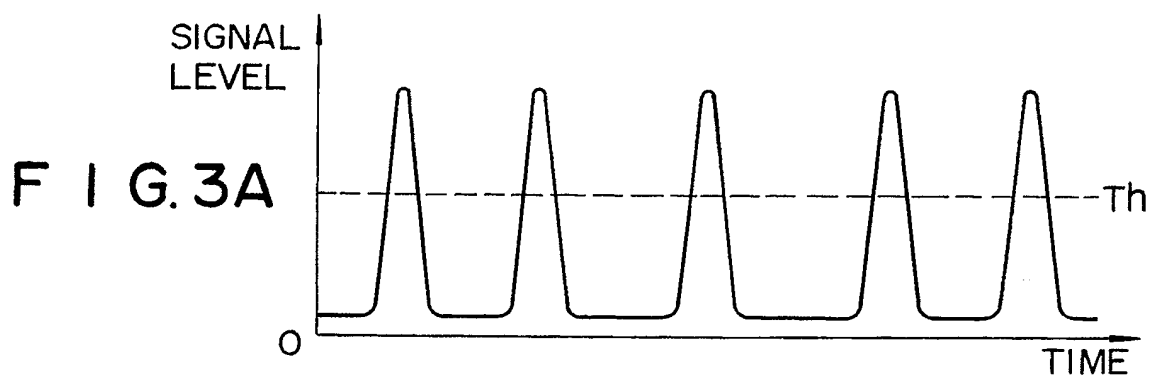
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12. A system according to claim 7, characterized in that said optical means (5, 8) includes a semiconductor laser (5) for modulating the recording light beam in response to the recording signal.

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**FIG. 4**

FIG. 5A

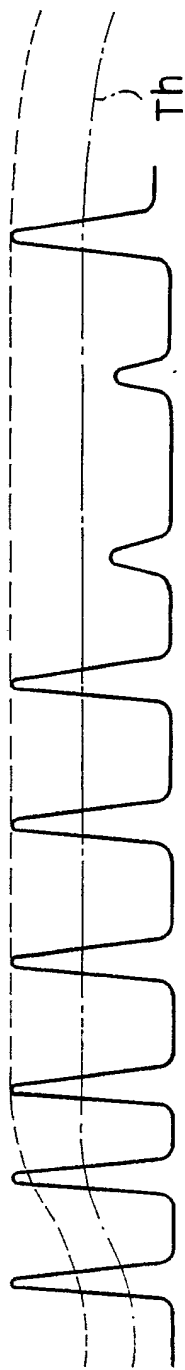
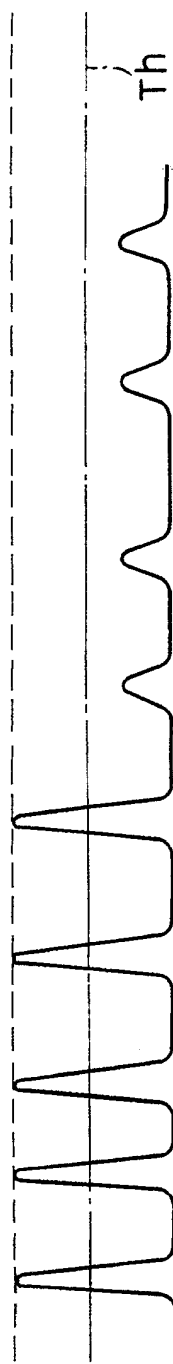
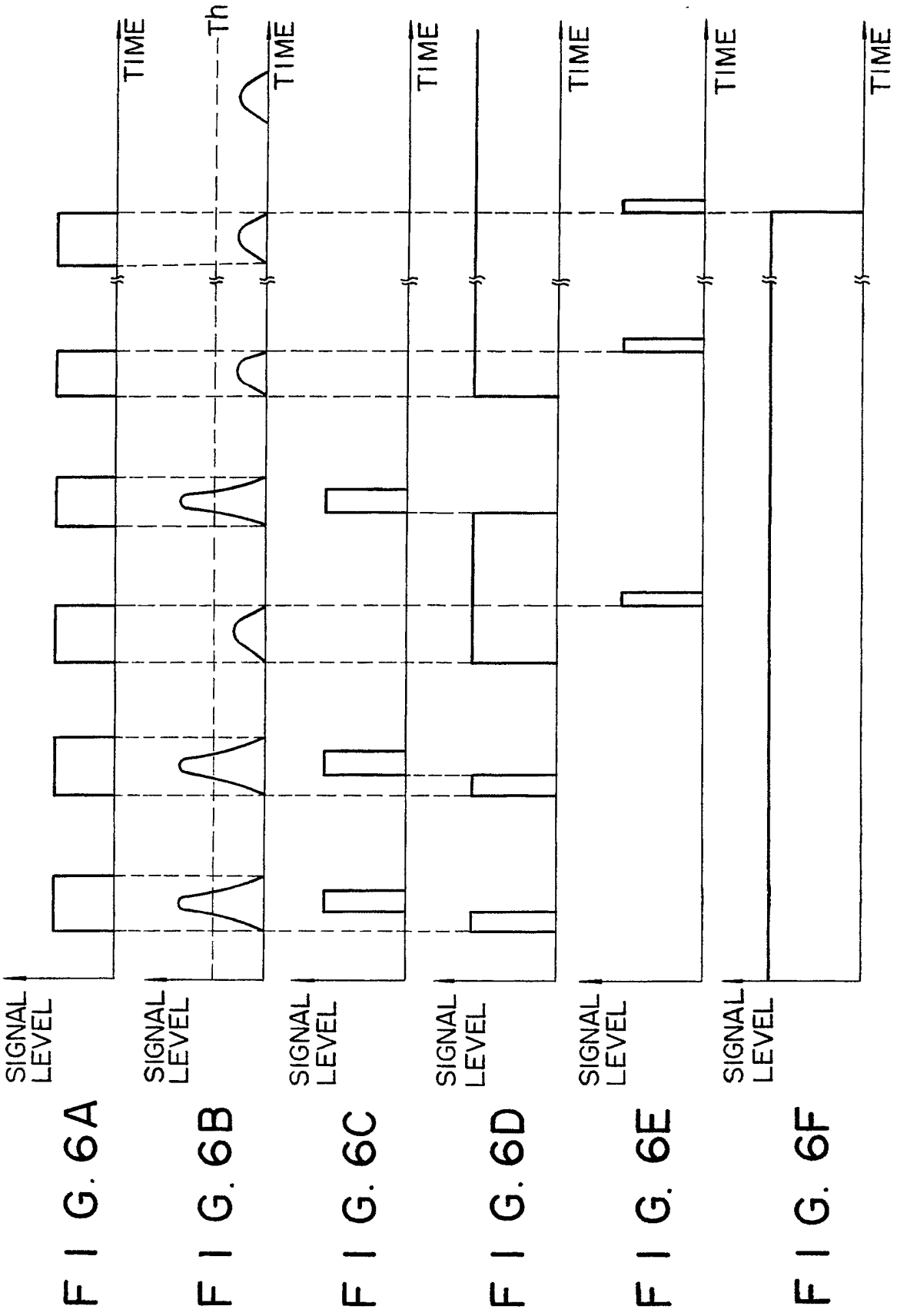
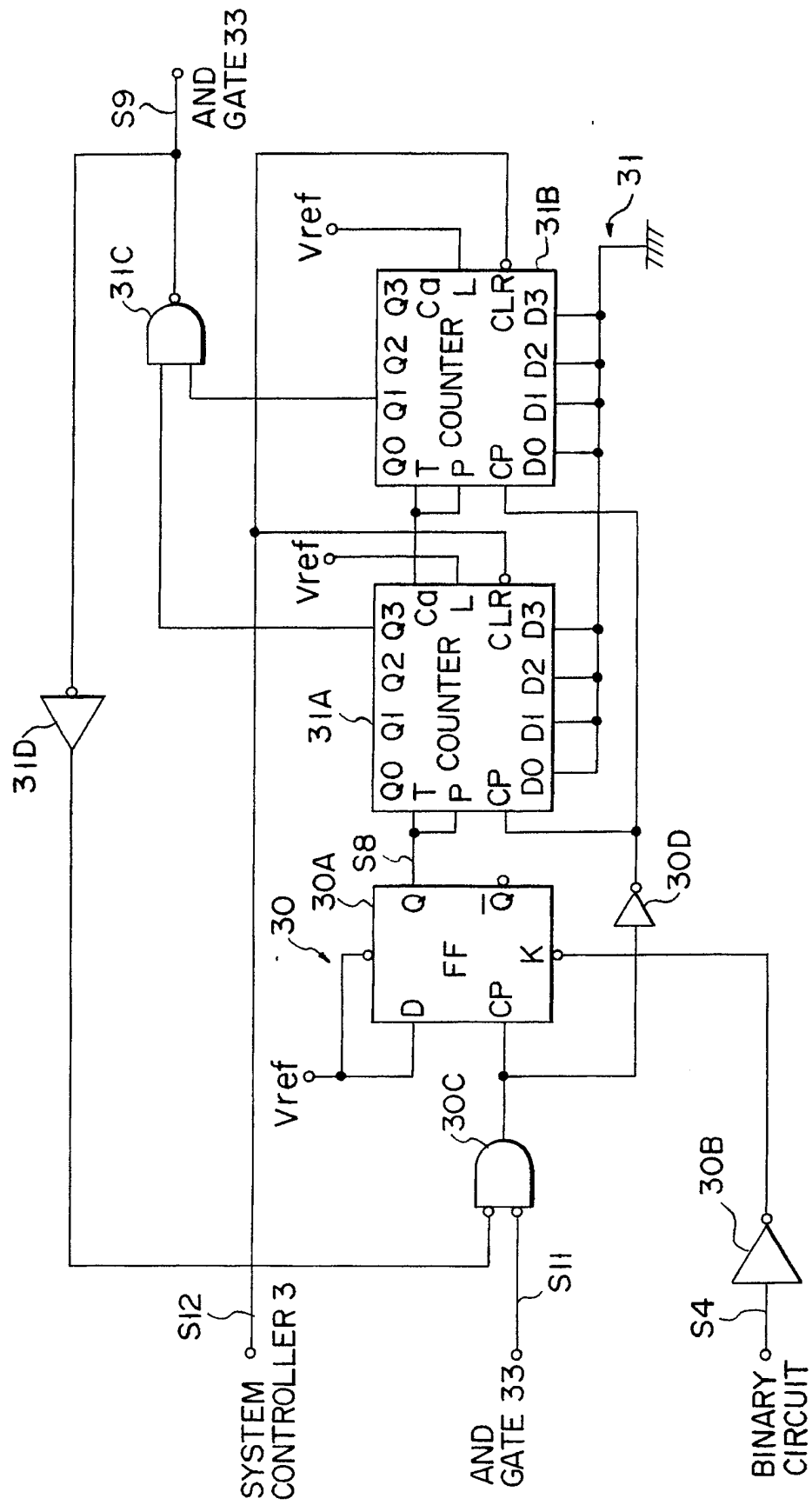


FIG. 5B









F I G. 7

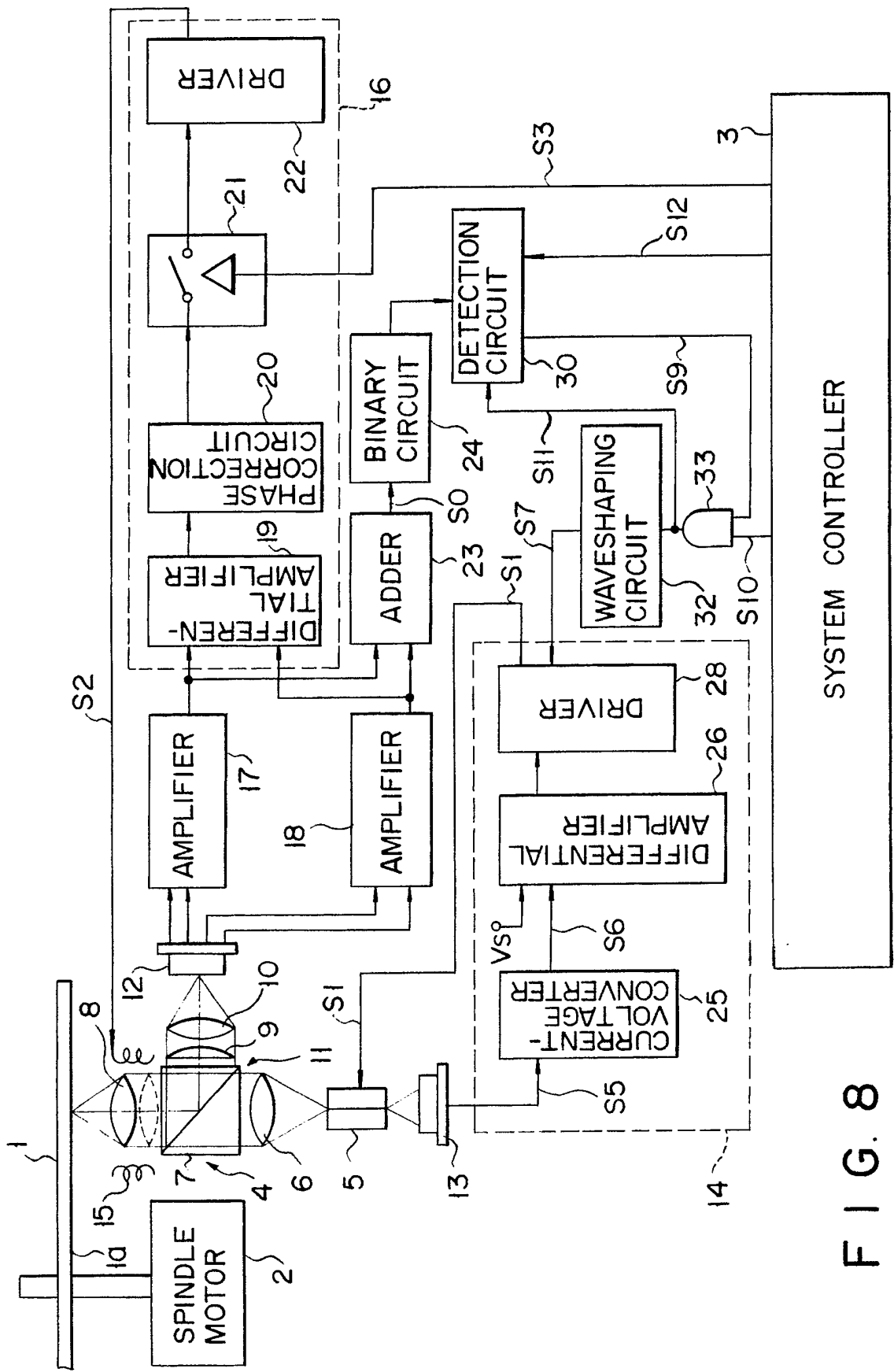
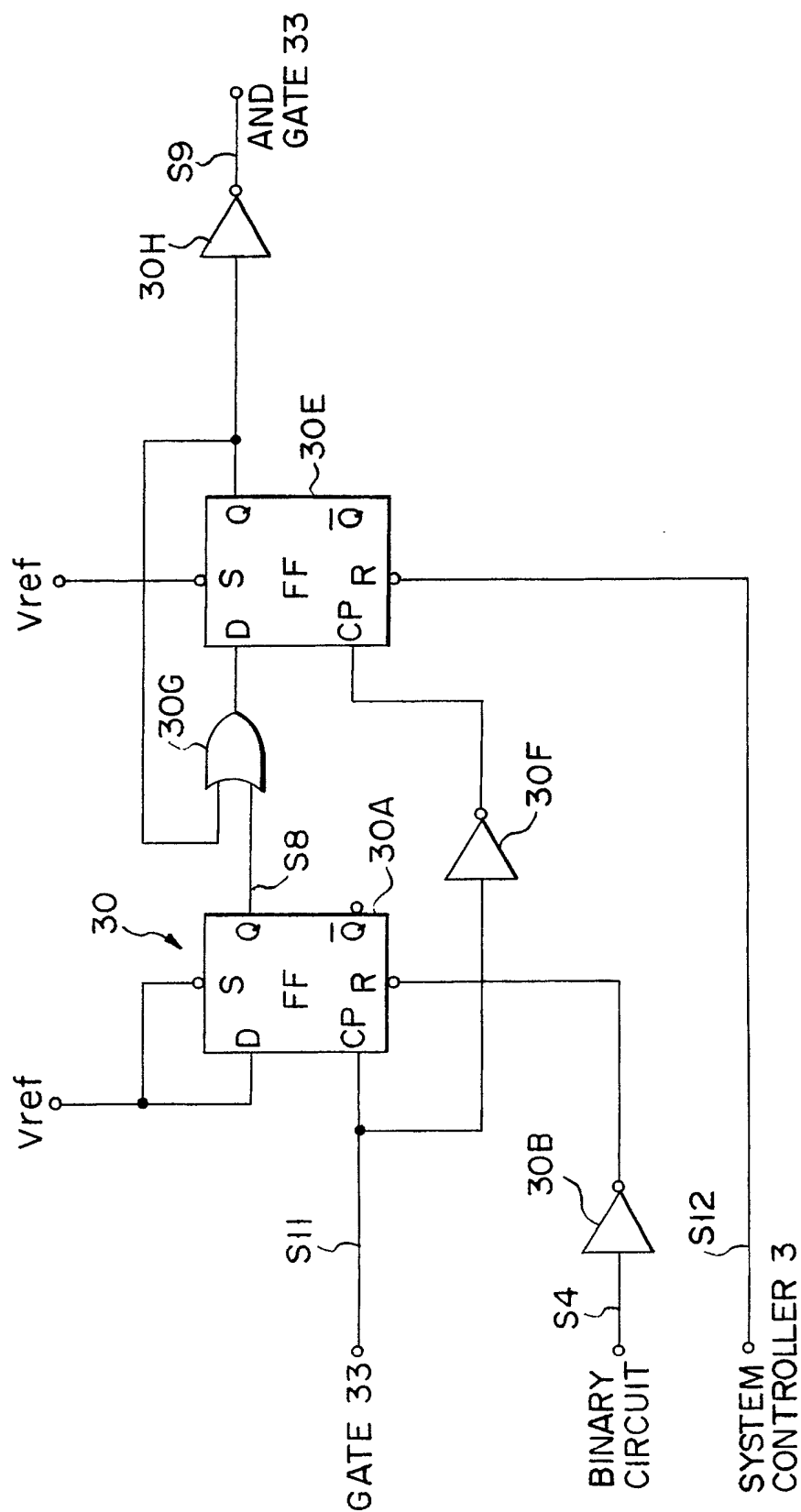


FIG. 8



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